

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

SUNDEEP M. BAIKAR, ET AL.

Application No.: 10/643,678

Filed: August 18, 2003

For: **Low Pin Count Docking Architecture for a  
Trusted Platform**

Art Group: 2139

Examiner: Kent Williams

Confirmation No.: 4611

**INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed). This IDS and IDS Citation Form are being submitted before the mailing of a final Office Action. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

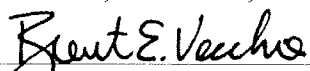
The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

The fee set in the amount of \$180.00 for submission of the Information Disclosure Statement is enclosed herewith. Please charge any additional fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: June 26, 2007

  
Brent E. Vecchia, Reg. No. 48,011

1279 Oakmead Parkway  
Sunnyvale, CA 94085-4040  
Telephone: (303) 740-1980

I hereby certify that this correspondence is being submitted electronically via EFS Web on the date shown below.



**Wendi Lou Rostan**

06-26-07

**Date**

Substitute for form 1449A/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(Use as many sheets as necessary)</i>	Complete if Known	
	<b>Application Number</b>	10/643,678
	<b>Filing Date</b>	August 18, 2003
	<b>First Named Inventor</b>	Bajikar, Sundeep
	<b>Group Art Unit</b>	2139
	<b>Examiner Name</b>	Williams, Kent
Sheet 1 of 2		Attorney Docket No: 42P16632

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		LAWTON, KEVIN , et al., "Running Multiple Operating Systems Concurrently on an IA32 PC Using Virtualization Techniques", <a href="http://www.plex86.org/research/paper.txt">http://www.plex86.org/research/paper.txt</a> , (11/29/1999),1-31	
		LUKE, JAHN , et al., "Replacement Strategy for Aging Avionics Computers", <u>IEEE AES Systems Magazine</u> , XP002190614,(March 1999),	
		MENEZES, ALFRED J., et al., "Handbook of Applied Cryptography", <u>CRC Press Series on Discrete Mathematics and its Applications</u> , Boca Raton, FL, XP002165287, ISBN 0849385237,(Oct. 1996),403-405, 506-515, 570	
		MENEZES, ALFRED J., et al., "Handbook of Applied Cryptography", <u>CRC Press LLC</u> , USA XP002201307, (1997),475	
		MOTOROLA, "M68040 User's Manual", (1993),1-1 to 8-32	
		NANBA, S. , et al., "VM/4: ACOS-4 Virtual Machine Architecture", <u>VM/4: ACOS-4 Virtual Machine Architecture</u> , IEEE, (1985), 171-178	
		RICHT, STEFAN , et al., "In-Circuit-Emulator Wird Echtzeittauglich", <u>Elektronik</u> , <u>Franzis Verlag GMBH</u> , Munchen, DE, Vol. 40, No. 16, XP000259620, (100-103), 8-6-1991	
		ROBIN, JOHN S., et al., "Analysis of the Pentium's Ability to Support a Secure Virtual Machine Monitor", <u>Proceedings of the 9th USENIX Security Symposium</u> , XP002247347, <u>Denver, Colorado</u> , (8/14/00), 1-17	
		ROSENBERG, JONATHAN B., "How Debuggers Work (Algorithms, Data Structures, and Architecture)", <u>Chapters 3 and 5 Hardware Debugger Facilities</u> , <u>Wiley Computer Publishing</u> , United States, (1996), pp. 42-43, 95, 96 and 99	
		ROSENBLUM, M. , "Virtual Platform: A Virtual Machine Monitor for Commodity PC", <u>Proceedings of the 11th Hotchips Conference</u> , (8/17/1999),185-196	
		RSA SECURITY, "Hardware Authenticators", <a href="http://www.rsasecurity.com/node.asp?id=1158">www.rsasecurity.com/node.asp?id=1158</a> , 1-2	
		RSA SECURITY, "RSA SecurID Authenticators", <a href="http://www.rsasecurity.com/products/securid/datasheets/SID_DS_0103.pdf">www.rsasecurity.com/products/securid/datasheets/SID_DS_0103.pdf</a> , 1-2	
		RSA SECURITY, "Software Authenticators", <a href="http://www.rsasecurity.com/node.asp?id=1313">www.rsasecurity.com/node.asp?id=1313</a> , 1-2	
		SAEZ, SERGIO , et al., "A Hardware Scheduler for Complex Real-Time Systems", <u>Proceedings of the IEEE International Symposium on Industrial Electronics</u> , XP002190615, (July 1999), 43-48	
		SCHNEIER, BRUCE , "Applied Cryptography: Protocols, Algorithm, and Source Code in C", <u>Wiley, John &amp; Sons, Inc.</u> , XP002939871; ISBN 0471117099, (Oct. 1995), 47-52	
		SCHNEIER, BRUCE , "Applied Cryptography: Protocols, Algorithm, and Source Code in C", <u>Wiley, John &amp; Sons, Inc.</u> , XP002138607; ISBN 0471117099, (Oct. 1995), 56-65	
		SCHNEIER, BRUCE , "Applied Cryptography: Protocols, Algorithms, and Source Code C", <u>Wiley, John &amp; Sons, Inc.</u> , XP0021111449; ISBN 0471117099, (Oct. 1995), 169-187	

EXAMINER

DATE CONSIDERED

Based on PTO/SB/08A(09-06) - Substitute Disclosure Statement Form (PTO-1449) as modified by BSTZ 03/26/07

\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional) 2 Applicant is to place a check mark here if English language Translation is attached

Substitute for form 1449A/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(Use as many sheets as necessary)</i>	<i>Complete if Known</i>	
	<b>Application Number</b>	10/643,678
	<b>Filing Date</b>	August 18, 2003
	<b>First Named Inventor</b>	Bajikar, Sundeep
	<b>Group Art Unit</b>	2139
	<b>Examiner Name</b>	Williams, Kent
Sheet 2 of 2	Attorney Docket No: 42P16632	

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		SCHNEIER, BRUCE , "Applied Cryptography: Protocols, Algorithms, and Source Code in C", 2nd Edition; Wiley, John & Sons, Inc., XP002251738; ISBN 0471128457, (Nov. 1995), 28-33; 176-177; 216-217; 461-473; 518-522	
		SHERWOOD, TIMOTHY , et al., "Patchable Instruction ROM Architecture", Department of Computer Science and Engineering, University of California, San Diego, La Jolla, CA, (Nov. 2001),	
		STANDARD MICROSYSTEMS CORPORATION, "LPC47N350, Legacy-Free Keyboard/Embedded Controller with SPI and LPC Docking Interface," SMSC LPC47N350, Preliminary, Revision 1.1, (1/14/2003), 346 pages	

EXAMINER

DATE CONSIDERED